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amplifiers optimized for DC, balanced, and AC performance. Measured performance shows significant tradeoffs in voltage gain, output resistance, transconductance bandwidth, input-referred flicker noise and offset voltage, and layout area. Tradeoffs and Optimization in Analog CMOS Design analog IC optimization can be formalized as a constrained optimization problem, that is, to maximize the sum of  $q c(x)$  of the optimization targets with all of the hard constraints being satisfied. 2.2 Multi-Step Simulation Environment We present an overview of our L2DC method in Figure 1. L2DC is able to find the optimized Learning to

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