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Altera High Definition Multimedia Interface

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Cyclone 10 GX HDMI 4Kp60 with Video and Image ... - Intel

This reference design demonstrates the Intel FPGA High Definition Multimedia Interface (HDMI) 2.0 video connectivity IP core with a video processing pipeline based on IP cores from the Intel FPGA Video and Image Processing (VIP) Suite.

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Abstract. HDMI® technology has become ubiquitous in connecting high-definition and emerging 4K/ultra high-definition digital devices. The HDMI specification combines uncompressed high-definition video, multichannel audio, and data in a single digital interface to provide crystal-clear digital quality over a single cable.

HDMI - Wikipedia

Altera High Definition Multimedia Interface

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High-Definition Multimedia Interface (HDMI) IP Core User Guide

The Altera High-Definition Multimedia Interface (HDMI) IP core provides support for next generation video display interface technology. The HDMI standard specifies a digital communications interface for use in both internal and external

What is the function of the HDMI cable? - CIE-Group
The board features on-board USB-Blaster™ II, high-speed

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High-Definition Multimedia Interface (HDMI®) | SpringerLink

Intel EK-10M08E144 MAX® 10 FPGA Evaluation Board. Intel EK-10M08E144 MAX® 10 FPGA Evaluation Board is an entry-level board for evaluating the MAX 10 FPGA technology and Enpirion® PowerSoC regulators. The EK-10M08E144 can develop designs for the 10M08S and 144-EQFP FPGA. The EK-10M08E144 can be used to measure FPGA power (VCC_CORE and VCC_IO). Use the FPGA's analog-to-digital converter ...

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MAX 10 FPGA Evaluation Boards - Intel | Mouser

HDMI (High-Definition Multimedia Interface) is a proprietary audio/video interface for transmitting uncompressed video data and compressed or uncompressed digital audio data from an HDMI-compliant source device, such as a display controller, to a compatible computer monitor, video projector, digital television, or digital audio device.

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